



FIG. 1

FIG. 2 is a schematic diagram of a device 100 in accordance with the present invention.

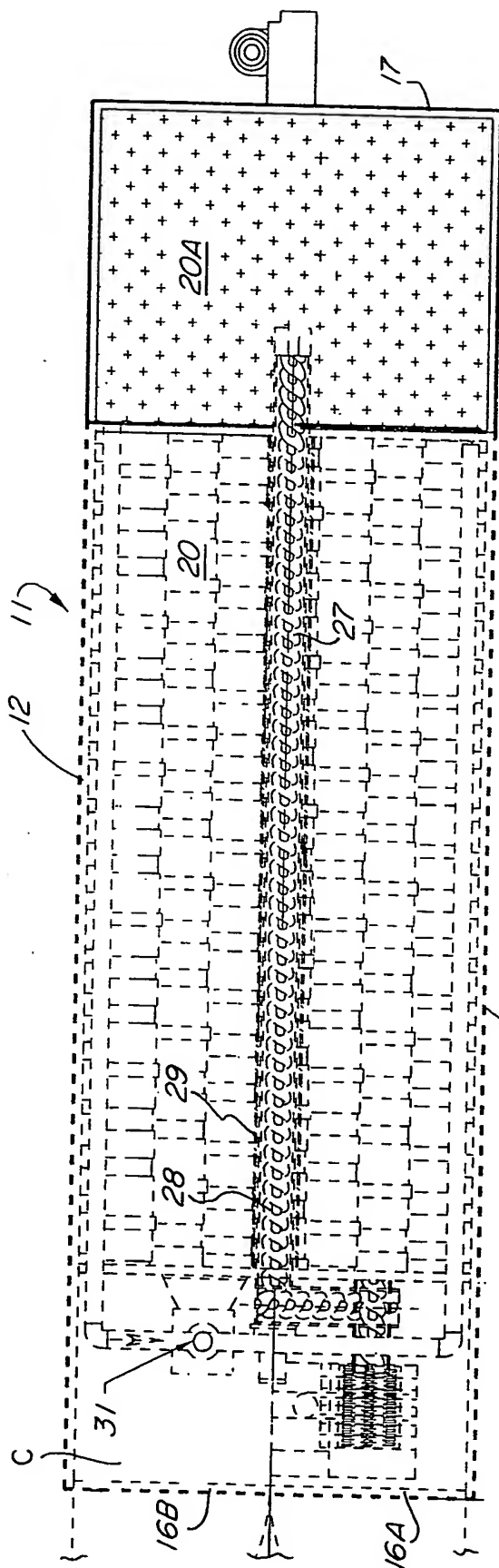


FIG. 2

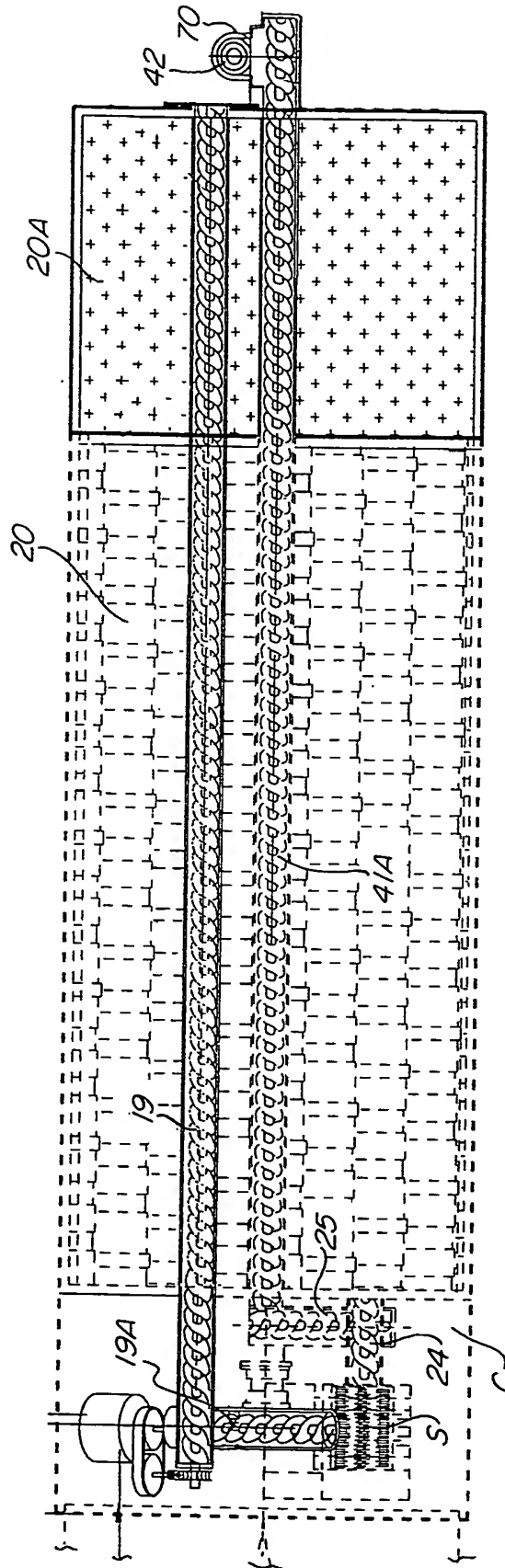


FIG. 3

FIG. 4

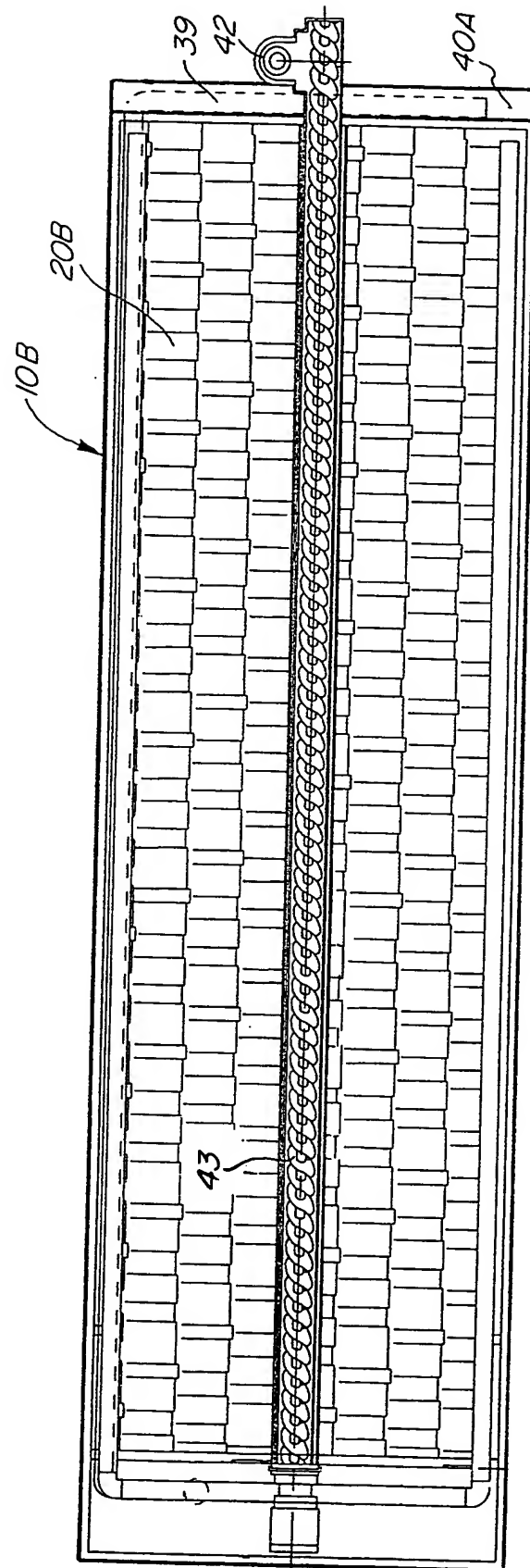


FIG. 4

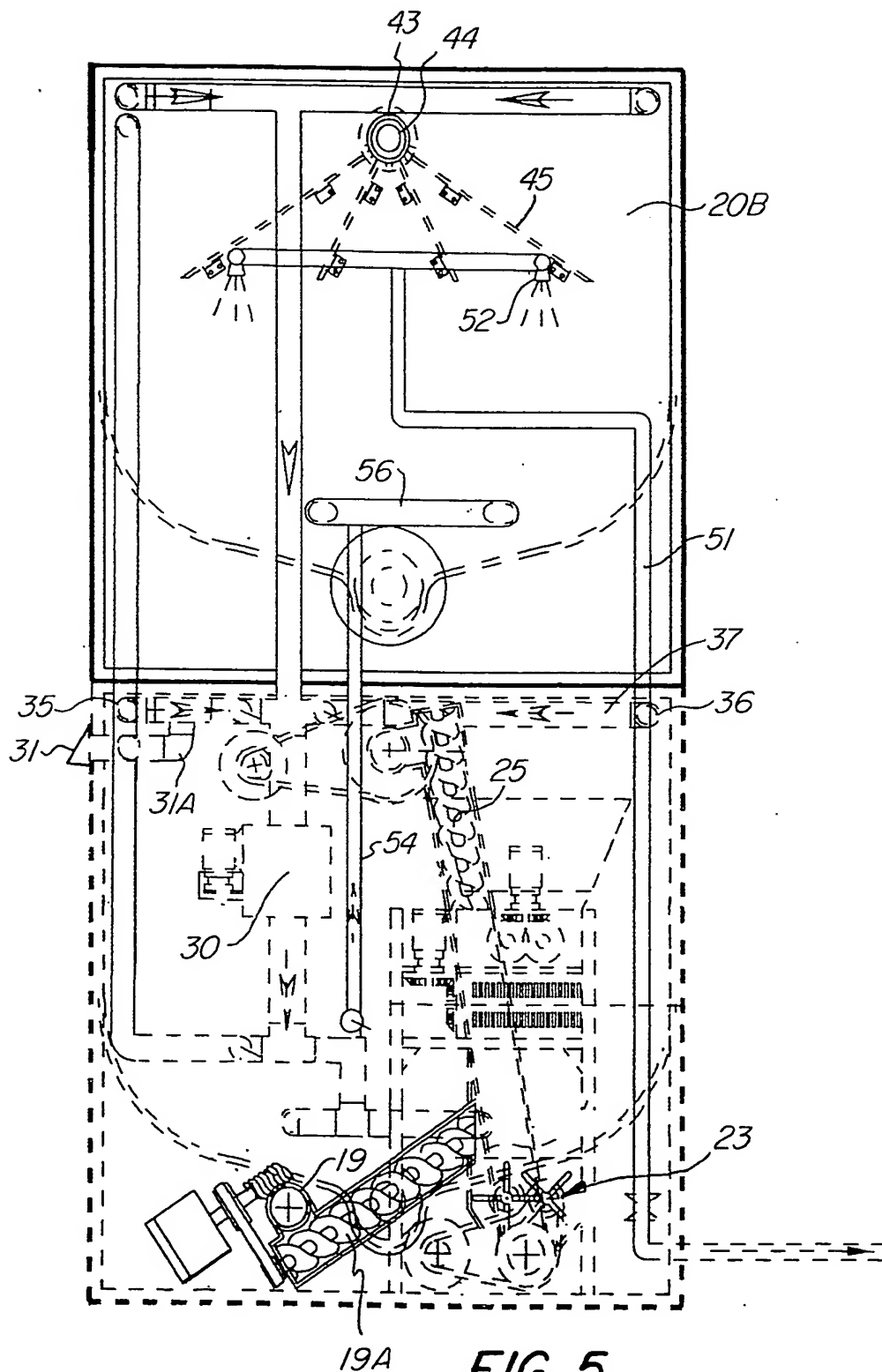


FIG. 5

FIG. 5B

FIG. 5A

FIG. 6

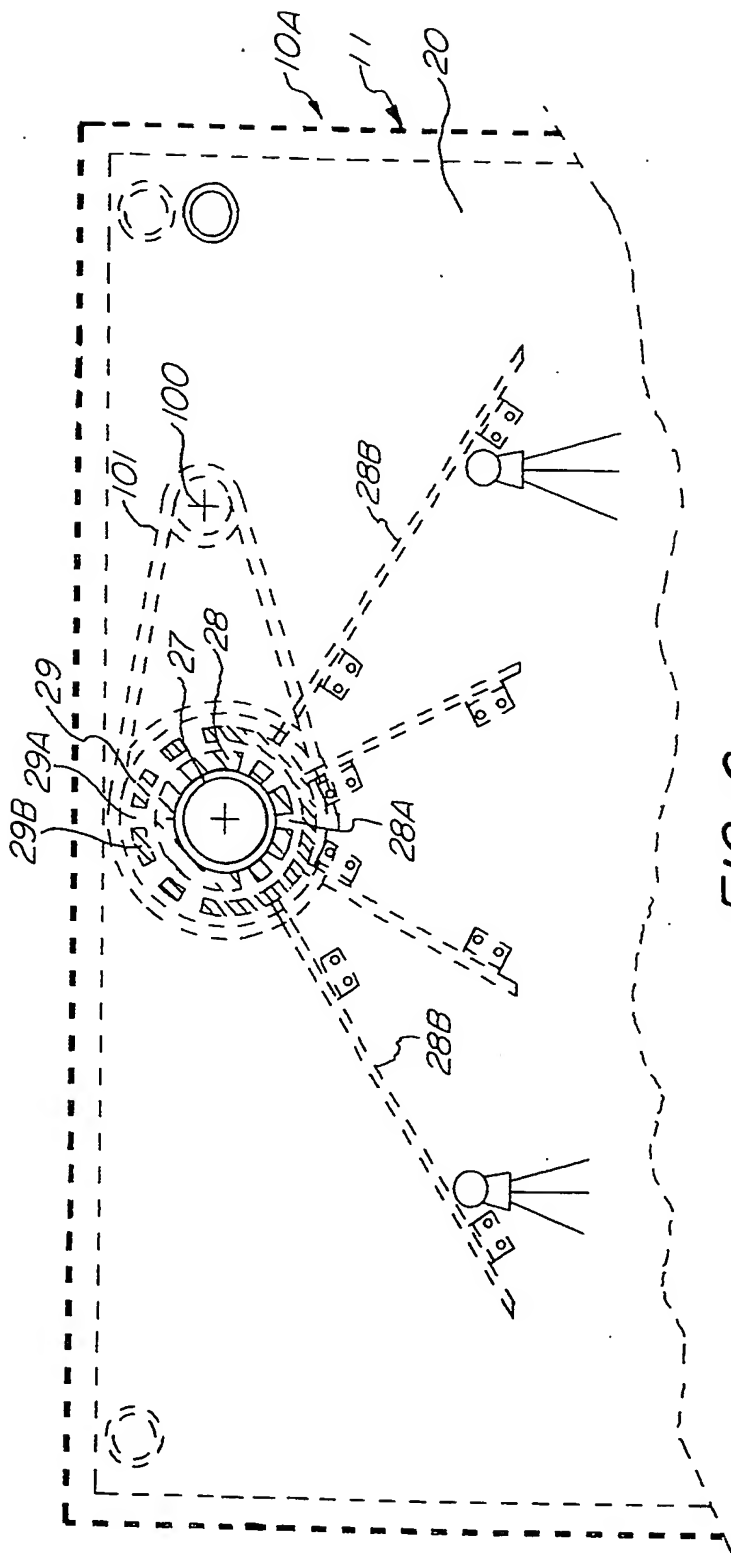


FIG. 6

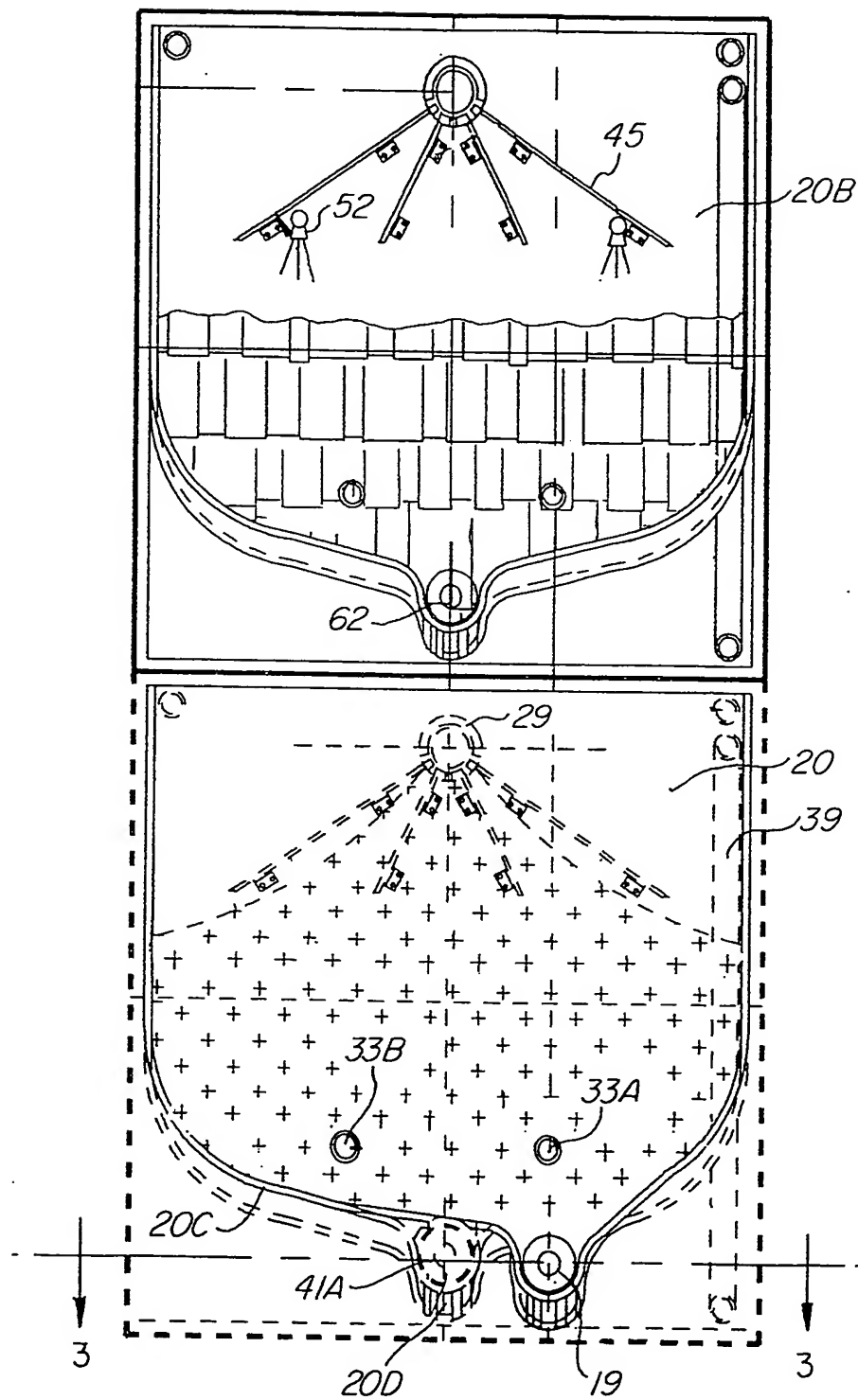


FIG. 7

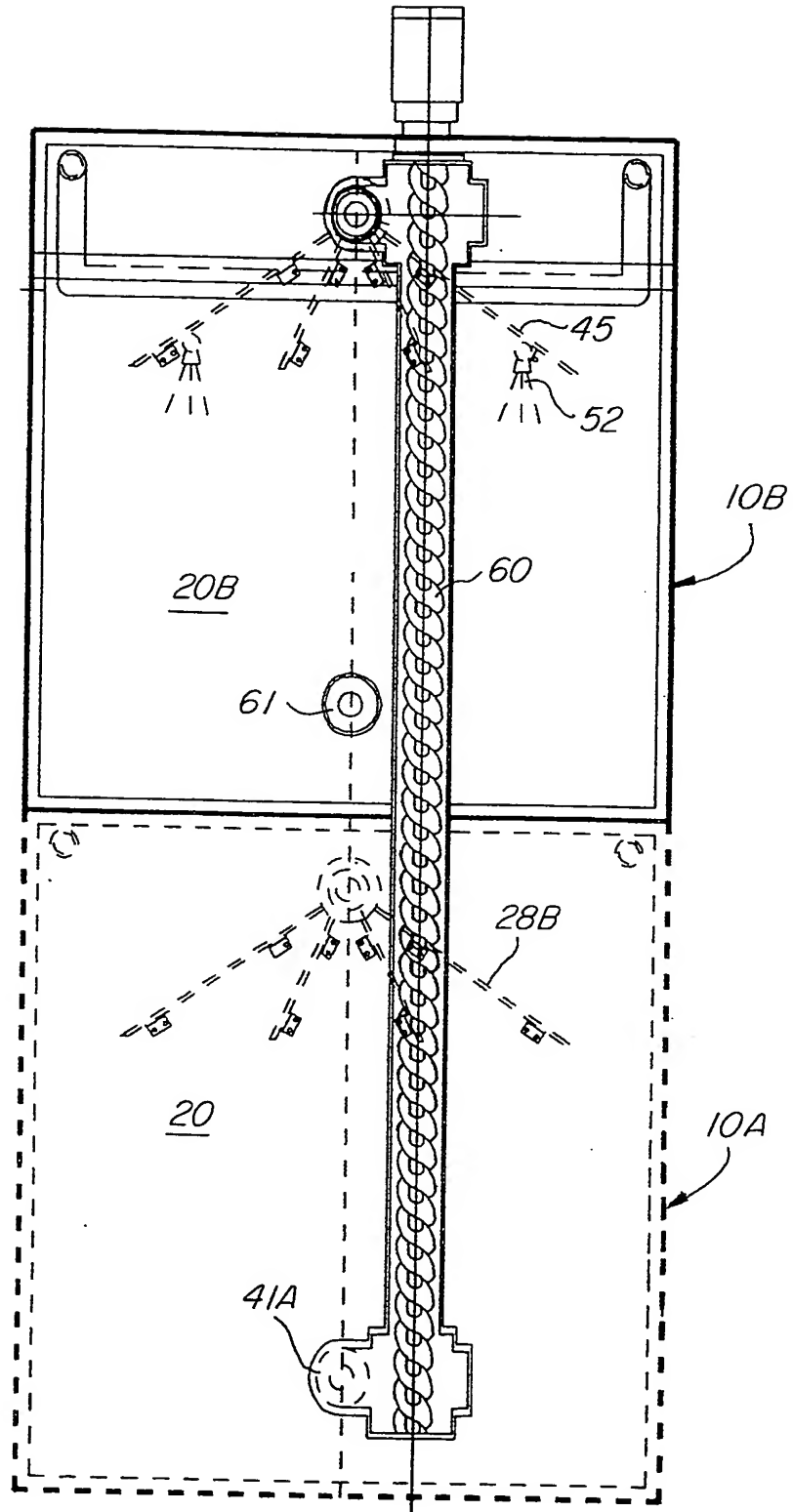


FIG. 8

